

**Department of Computer Science
Mata Sundri College For Women**

University of Delhi

Notice Board

Course Name: (570)-B.SC.(HONS.) COMPUTER SCIENCE

Format of Cells: [Part & Sem ,Paper Code, Room No, Faculty Code, Lecture Type, Section]

Period	I	II	III	IV	V	VI	VII	VIII
Period Time	9.00 TO 10.00	10.00 TO 11.00	11.00 TO 12.00	12.00 TO 1.00 P.M	1.00 P.M TO 2.00 P.M	2.00 P.M TO 3.00 P.M	3.00 P.M TO 4.00 P.M	4.00 P.M TO 5.00 P.M
Monday	[III & V, Microprocessor (DSE-1), 403, DR. MEGHA GUPTA, TH,NILL],	[III & V, Microprocessor (DSE-1), 403, DR. MEGHA GUPTA, TH,NILL],	[III & V, System Programming (DSE-2), 403,DR. NIDHI, TH,NILL],	[III & V, System Programming (DSE-2), 403,DR. NIDHI, TH,NILL],	[III & V, Internet Technologies (CORE),402, MS. VARSHA, PR,NILL], - G1 [III & V, Theory of Computation (CORE),402-A, MS. VIJAYA GOEL, TUT,NILL]- G2	[III & V, Internet Technologies (CORE),402-A, MS. VARSHA, PR,NILL]- G2 , [III & V, Microprocessor (CORE),402, MS. VIJAYA GOEL, PR,NILL]- G1		
Tuesday	[III & V, Theory of Computation (CORE),403, MS. VIJAYA GOEL, TH,NILL]	[III & V, Theory of Computation (CORE),403, MS. VIJAYA GOEL, TH,NILL]	[III & V, System Programming (DSE-2),402, DR. NIDHI PR,NILL], - G1 [III & V, Microprocessor (DSE-2),402-A, DR. MEGHA GUPTA, PR, NILL]- G2	[III & V, System Programming (DSE-2),402-A, DR. NIDHI PR,NILL], - G2 [III & V, Microprocessor (DSE-2),402, DR. MEGHA GUPTA, PR, NILL]- G1	[III & V, Internet Technologies (CORE),402, MS. VARSHA, PR,NILL], - G1 [III & V, Internet Technologies (CORE),402-A, MS. ASHIMA, PR,NILL], - G2	[III & V, Internet Technologies (CORE),402-A, MS. VARSHA, PR,NILL], - G2 [III & V, System Programming (DSE-2),402, MS. PRIYANKA GUPTA PR,NILL], - G1		
Wednesday	[III & V, Microprocessor (DSE-1), 403, DR. MEGHA GUPTA, TH,NILL],	[III & V, Microprocessor (DSE-1), 403, DR. MEGHA GUPTA, TH,NILL],	[III & V, Theory of Computation (CORE),403, MS. VIJAYA GOEL, TH,NILL]	[III & V, System Programming (DSE-2),402, DR. NIDHI PR,NILL], - G1 [III & V, Internet Technologies (CORE),402-A, MS. VARSHA, PR,NILL], - G2	[III & V, System Programming (DSE-2),402-A, DR. NIDHI PR,NILL], - G2 [III & V, Internet Technologies (CORE),402, MS. VARSHA, PR,NILL], - G1	[III & V, Internet Technologies (CORE),402, MS. ASHIMA, PR,NILL], - G1 [III & V, System Programming (DSE-2),402-A, MS. PRIYANKA GUPTA PR,NILL], - G2		
Thursday	[III & V, System Programming (DSE-2), 403,DR. NIDHI, TH,NILL],	[III & V, System Programming (DSE-2), 403,DR. NIDHI, TH,NILL],	[III & V, Internet Technologies (CORE),403, MS. VARSHA, TH,NILL],	[III & V, Internet Technologies (CORE),403, MS. VARSHA, TH,NILL],	[III & V, Theory of Computation (CORE),402, MS. VIJAYA GOEL, TUT,NILL]- G1 [III & V, Microprocessor (DSE-2),402-A, DR. MEGHA GUPTA, PR, NILL]- G2	[III & V, Microprocessor (DSE-2),402-A, MS. VIJAYA GOEL, PR, NILL], - G2 [III & V, Microprocessor (DSE-2),402, DR. MEGHA GUPTA, PR, NILL]- G1		
Friday	[III & V, Internet Technologies (CORE),403, MS. VARSHA, TH,NILL],	[III & V, Internet Technologies (CORE),403, MS. VARSHA, TH,NILL],	[III & V, System Programming (DSE-2),402, DR. NIDHI PR,NILL], - G1 [III & V, Microprocessor (DSE-2),402-A, DR. MEGHA GUPTA, PR, NILL]- G2	[III & V, System Programming (DSE-2),402-A, DR. NIDHI PR,NILL], - G2 [III & V, Microprocessor (DSE-2),402, DR. MEGHA GUPTA, PR, NILL]- G1	[III & V, Theory of Computation (CORE),403, MS. VIJAYA GOEL, TH,NILL]	[III & V, Theory of Computation (CORE),403, MS. VIJAYA GOEL, TH,NILL]		
Saturday			OFF DAY					

Teacher's Sign

Teacher-in-Charge

Time Table Convener

Principal

Paper:- 32341501: INTERNET TECHNOLOGIES (B.SC. (HONS) COMPUTER SCIENCE, 32341502: Theory of Computation, 32347501: SYSTEM PROGRAMMING, 32347504: MICROPROCESSOR.
Faculty :- (AD192) Mrs. Vijaya Goel, (AD193) Ms. Priyanka Gupta, (AD203) Ms. Ashema Hasti, (AD204) Dr. Nidhi, (AD234) Dr. Megha Gupta, (AD259) Ms. Varsha.